

FIG.1

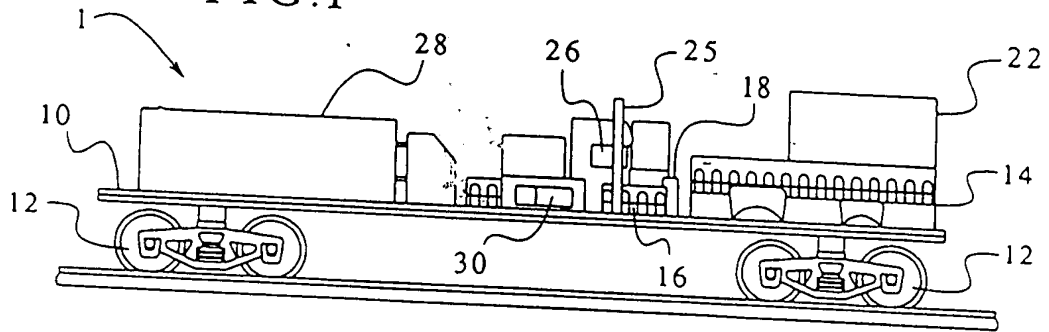


FIG.2

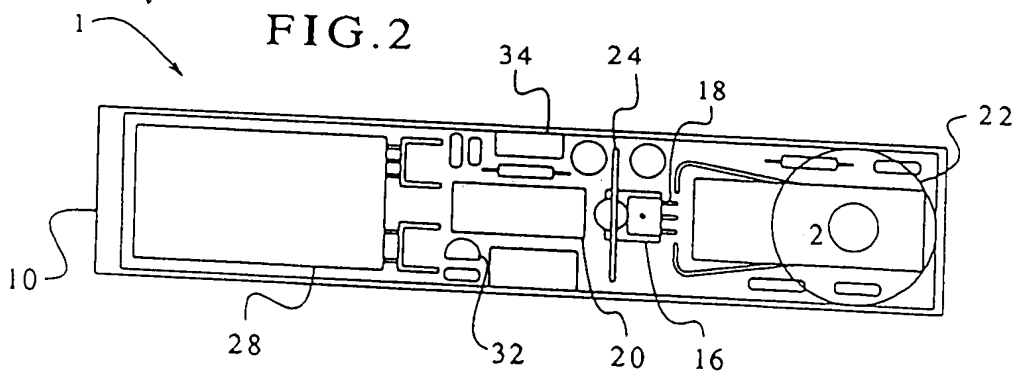
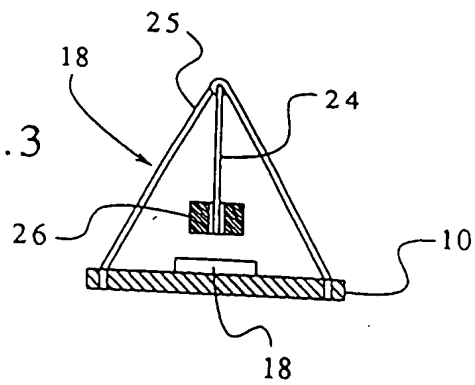
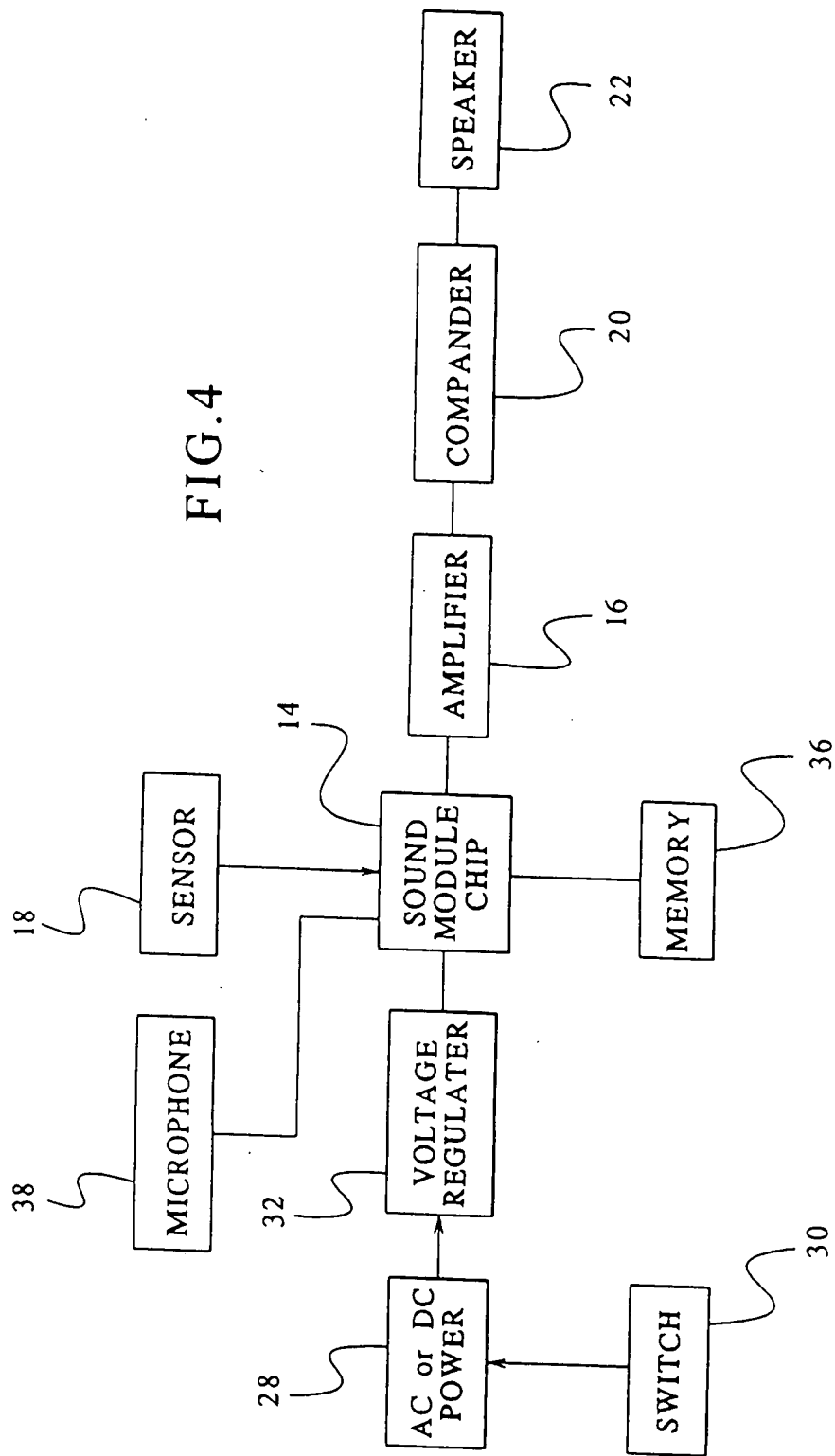
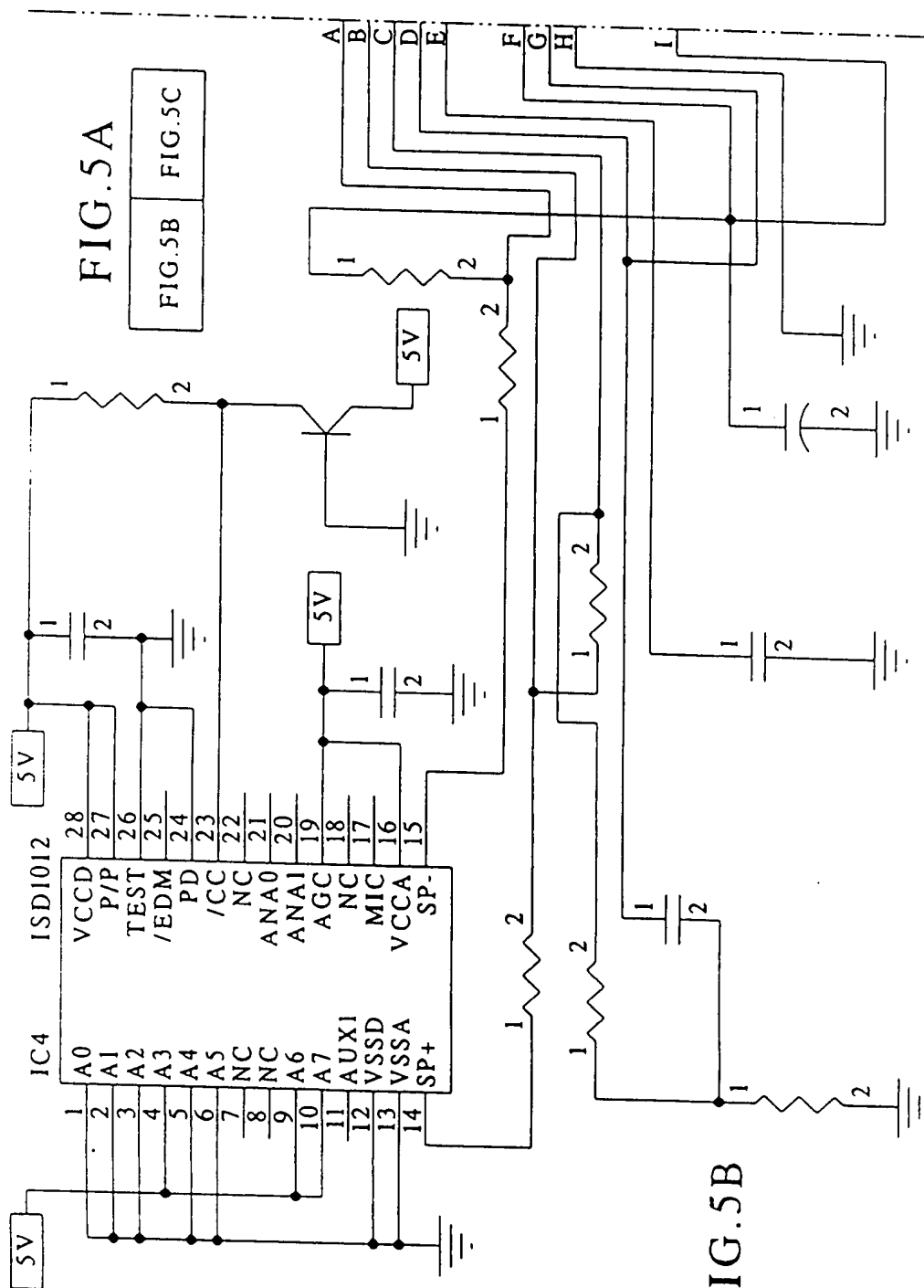


FIG.3







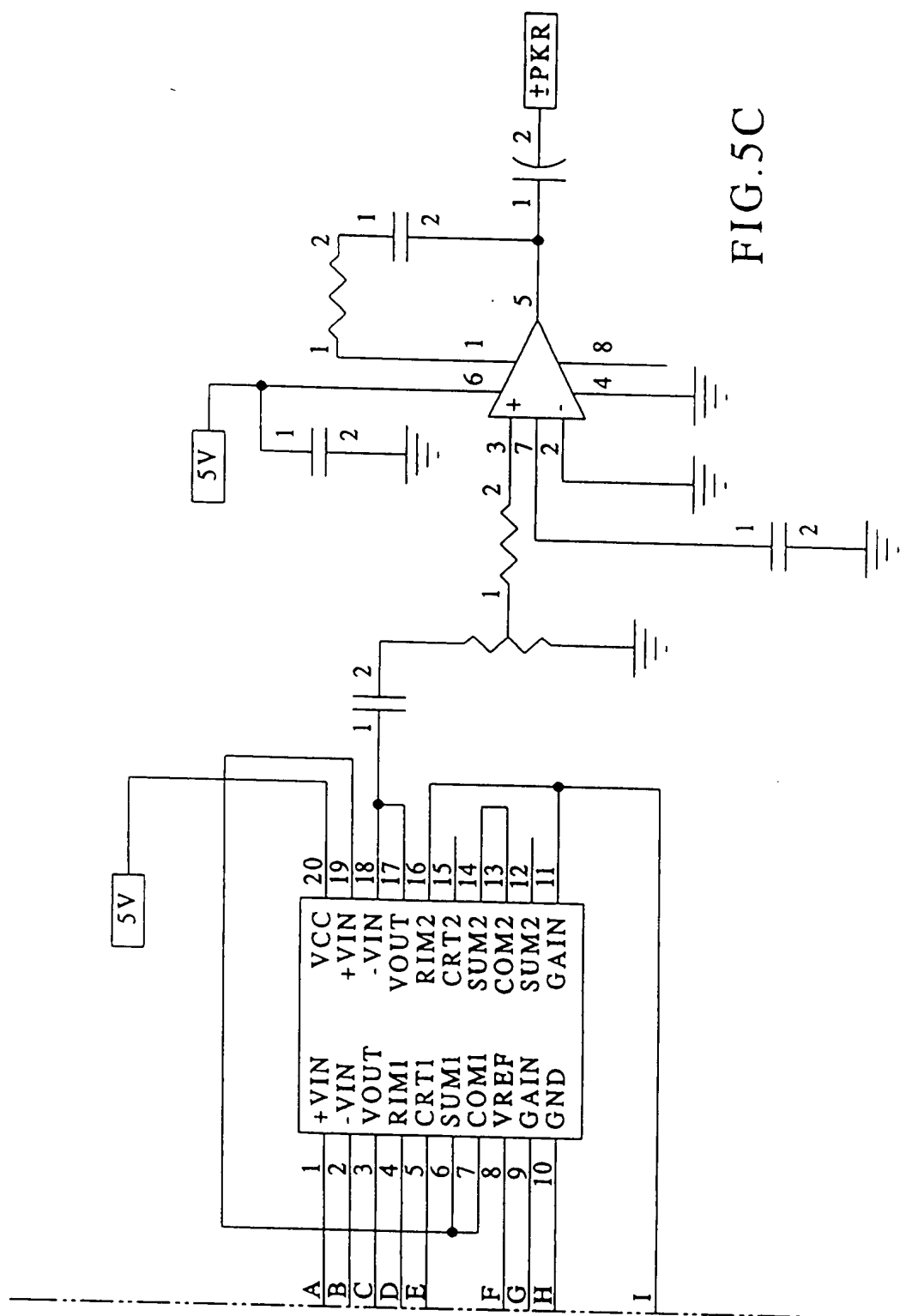


FIG. 5C

FIG.6

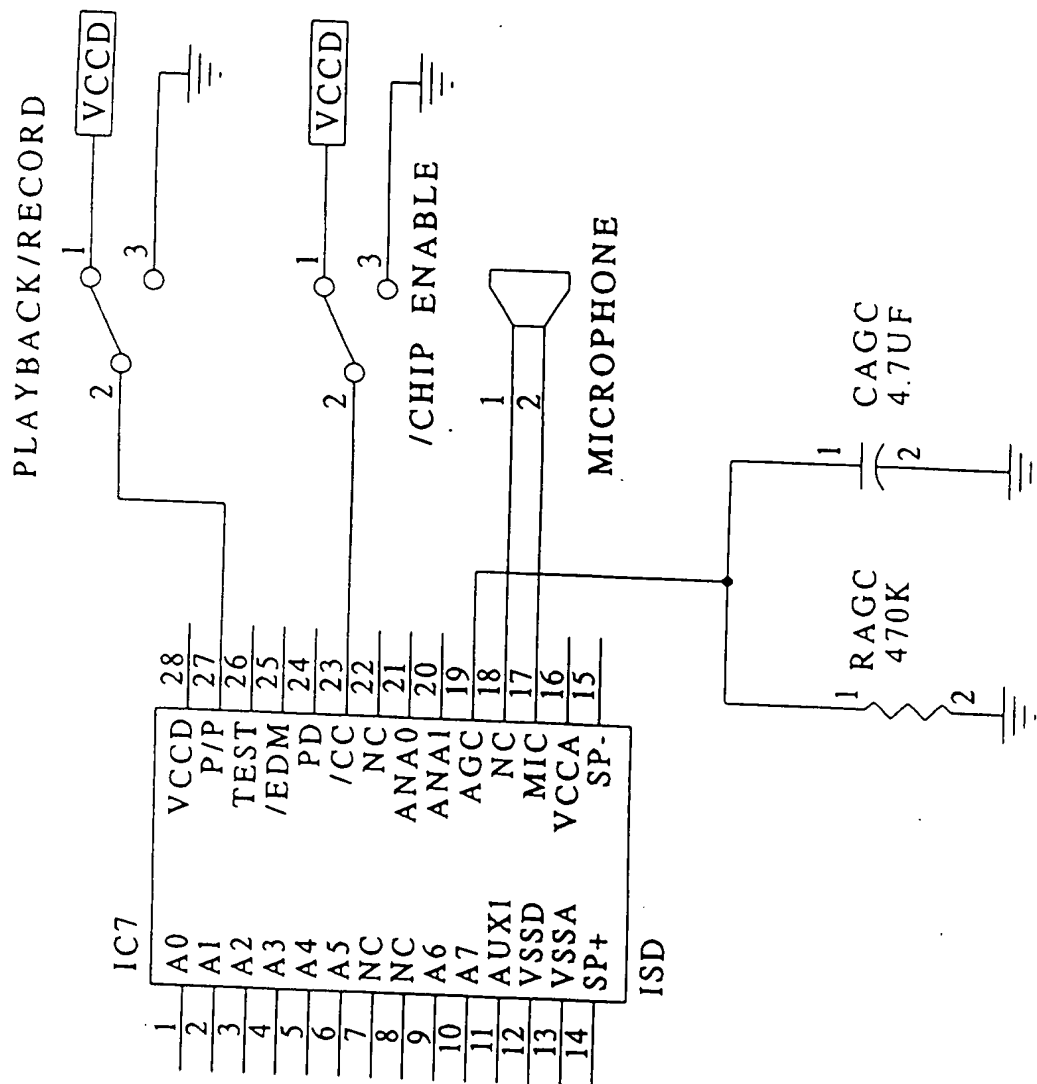


FIG. 7A

FIG. 7B

FIG. 7C

104

103

IC2  
93C46/56/57/66  
EEPROM

CS VCC  
CLK NC  
DI DRG  
DO VSS

+5V OUTPUT LINES

105

1-4

B  
C  
D  
E  
F  
G  
H  
I

102

XTAL1

C5 C6

HALL EFFECT SYNC SENSOR 112

107

Q3

Q5

Q6

Q4

FILTER NETWORK

MOT1 MOTOR

+V-UNREG

J3

J4

J5

D1

R3

LED1

OPTO1 H11L2Q

K

L

M

N

O

P

Q

R

1.5 BRIDGE BR1

AC +

AC -

+V-UNREG

C1

QUICK BLOW PICO FUSES

F1

F2

J2

FIG.7B	FIG.7C
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FIG. 7B

[illegible]

FIG.7C

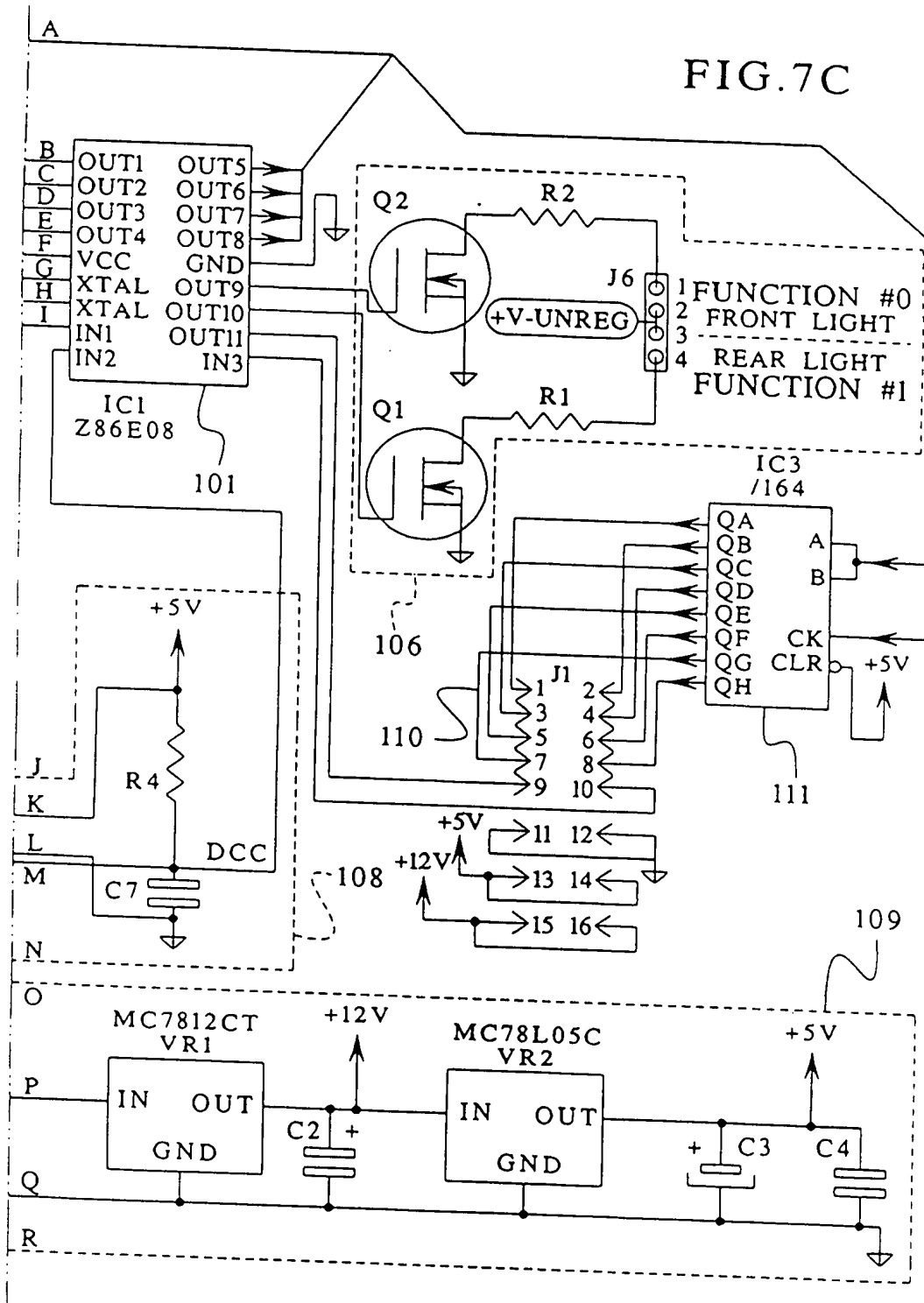


FIG.8A

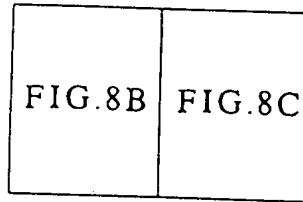
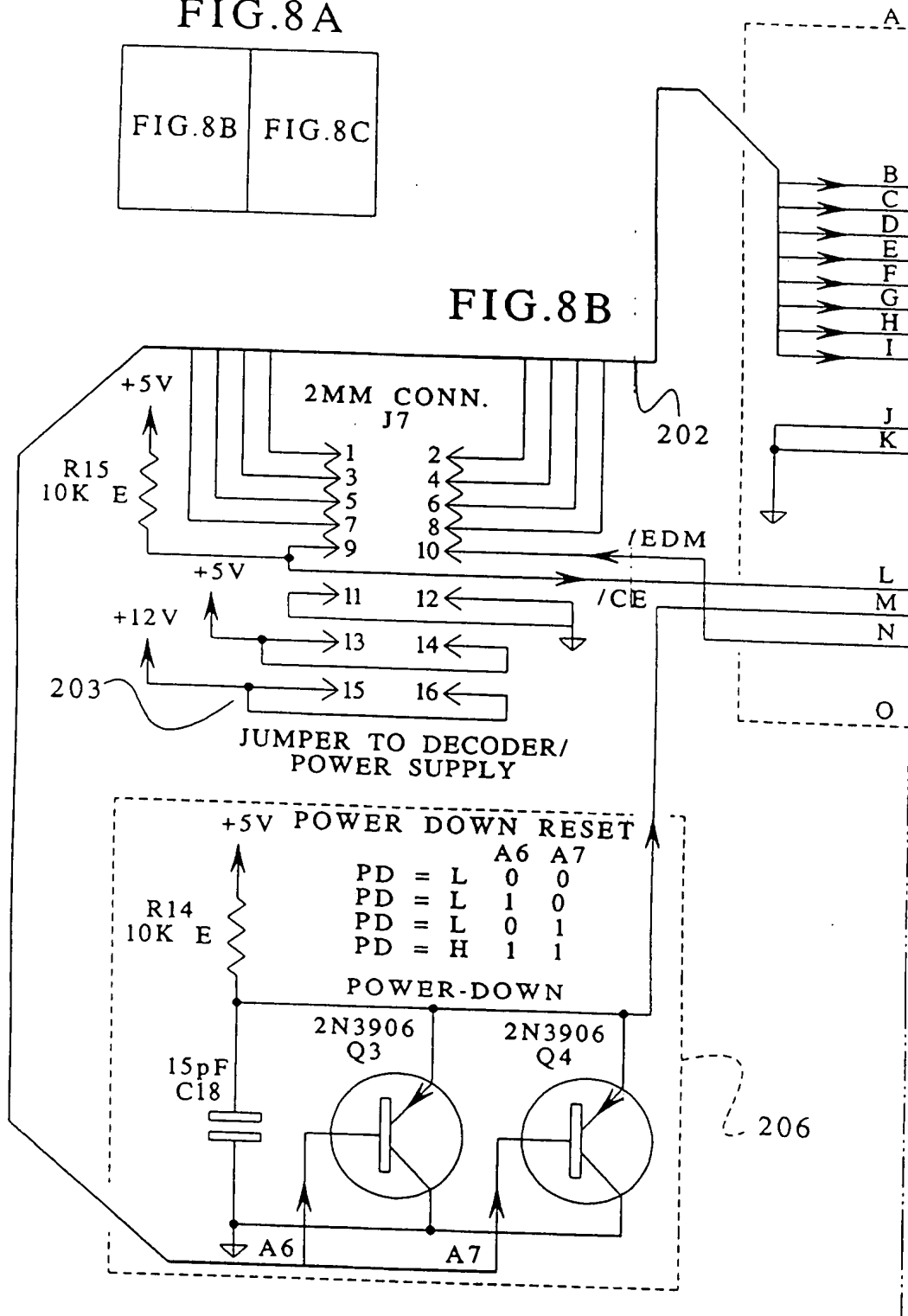


FIG.8B





00000-1-574550

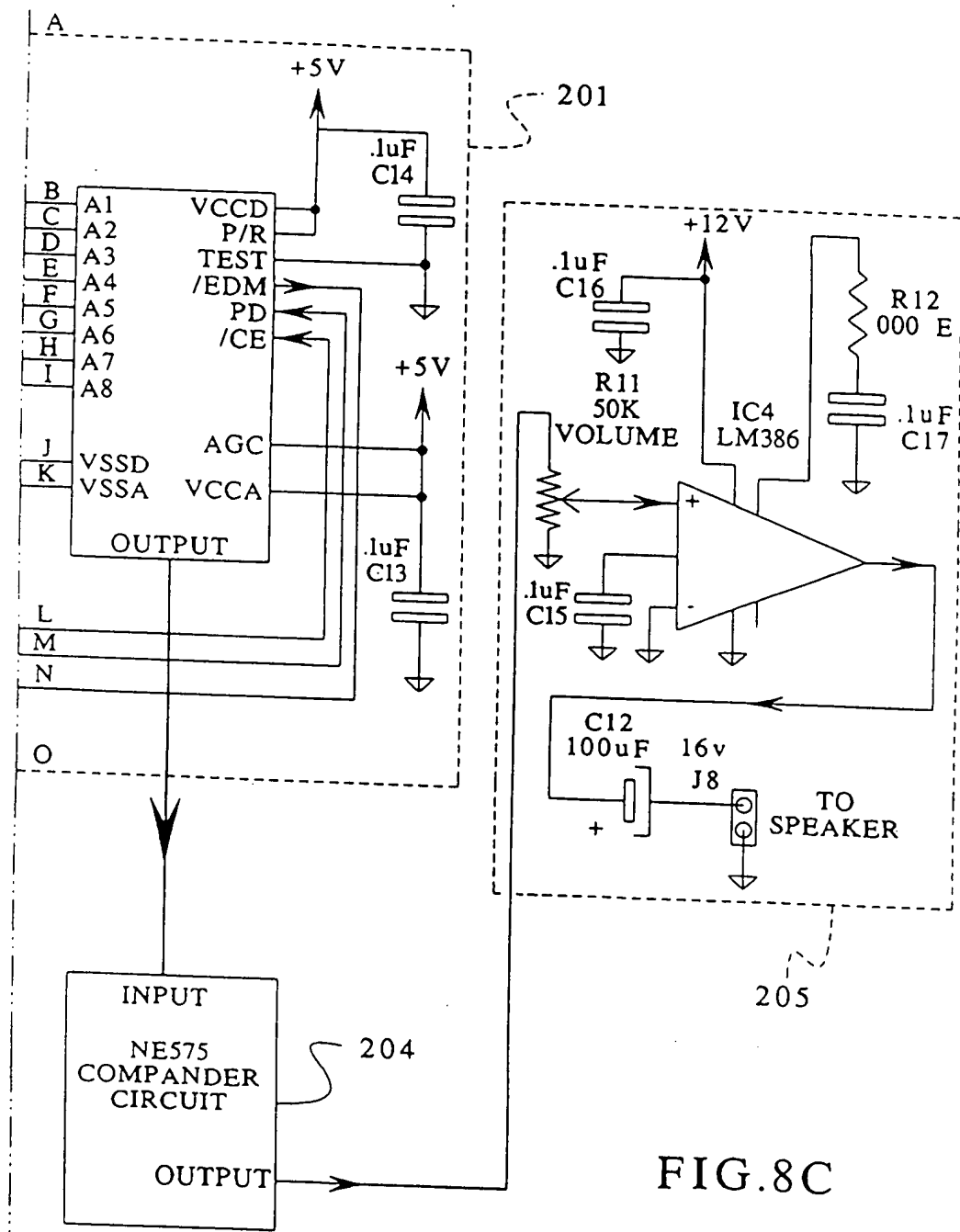


FIG. 8C

FIG.9A

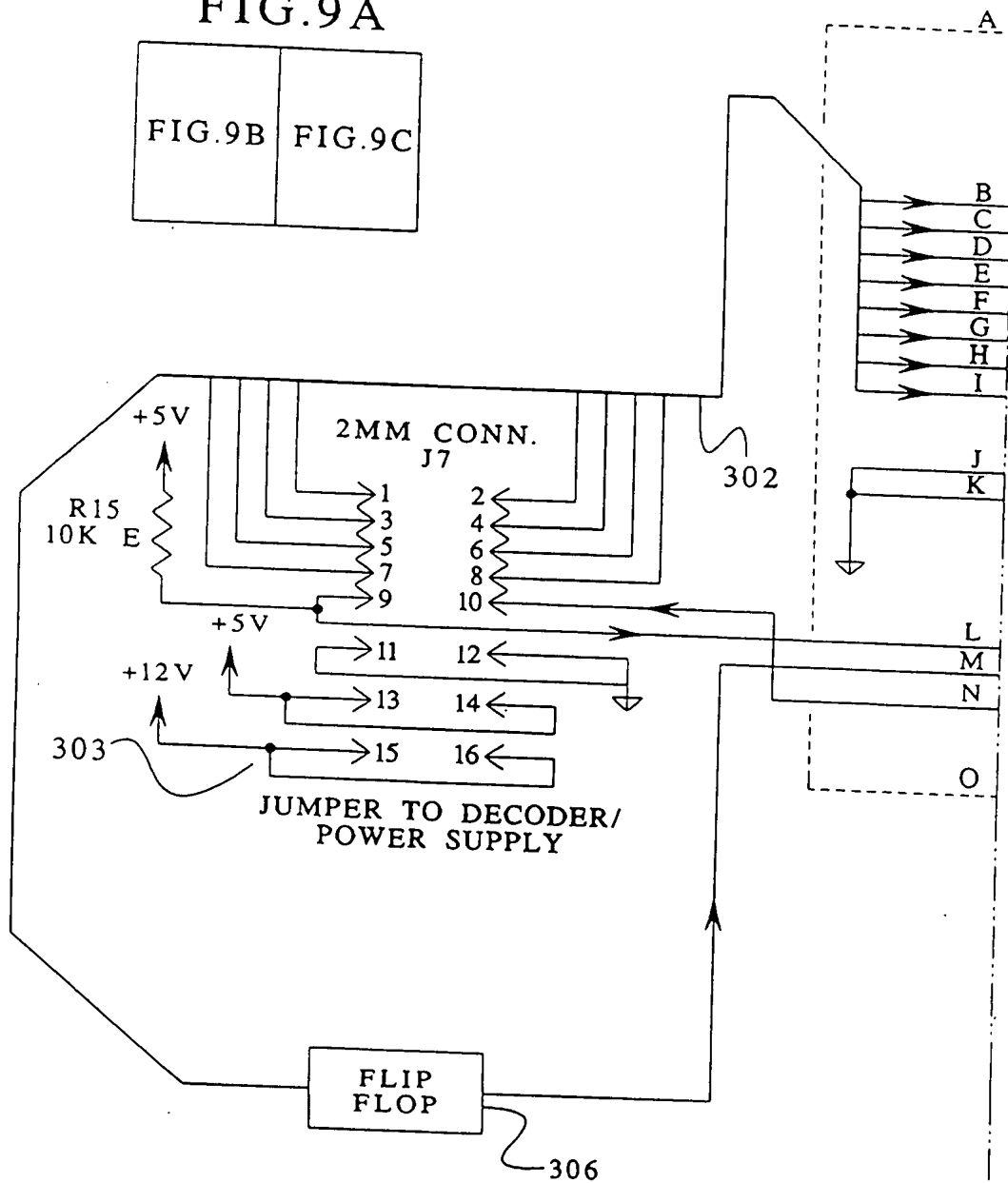
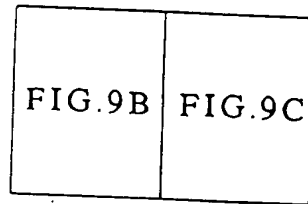


FIG.9B

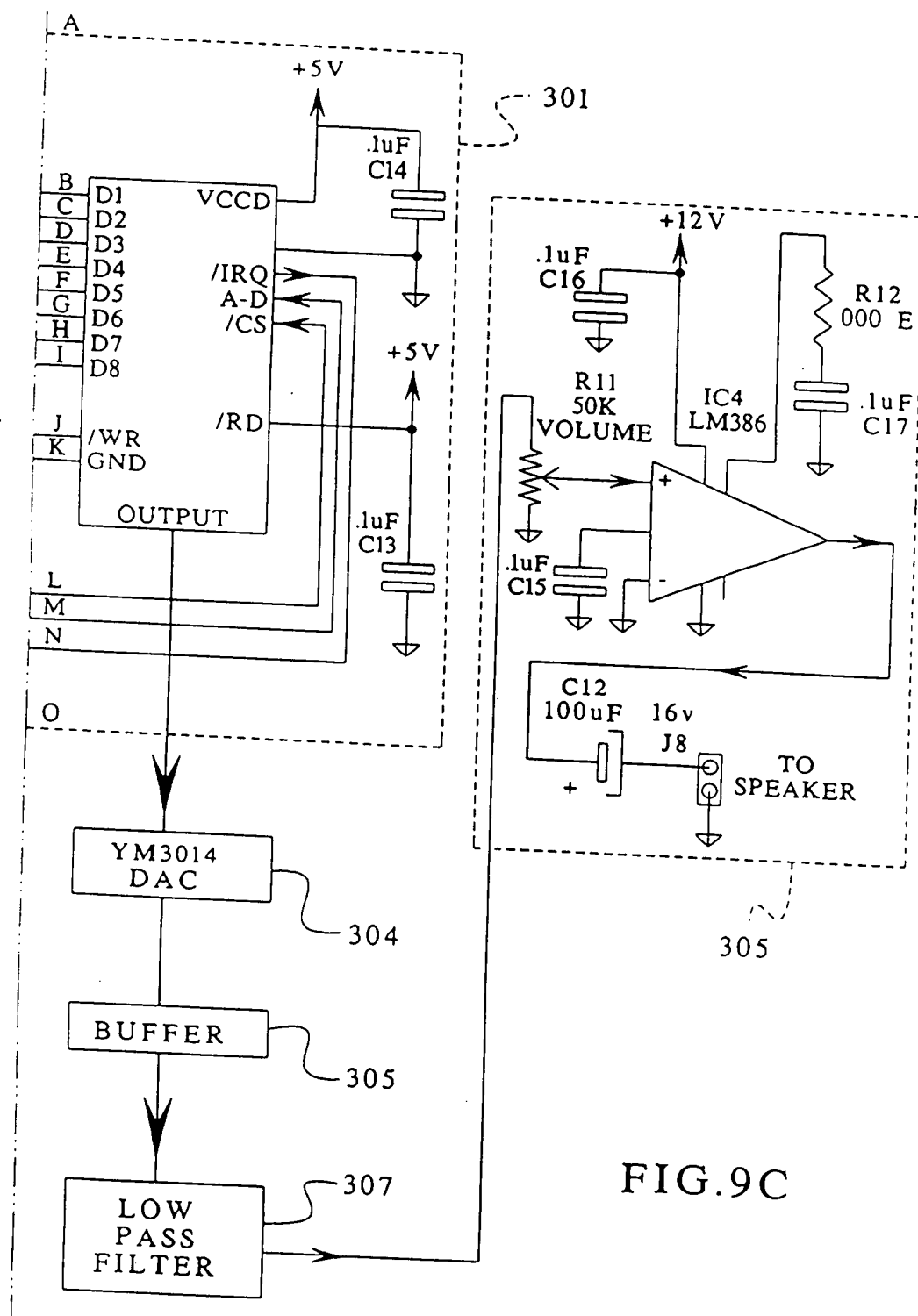


FIG. 10

DCC BASELINE PACKET 401  
 1 = 58us (52us-64us) 402  
 0 > 100us (90us-10000us) 403

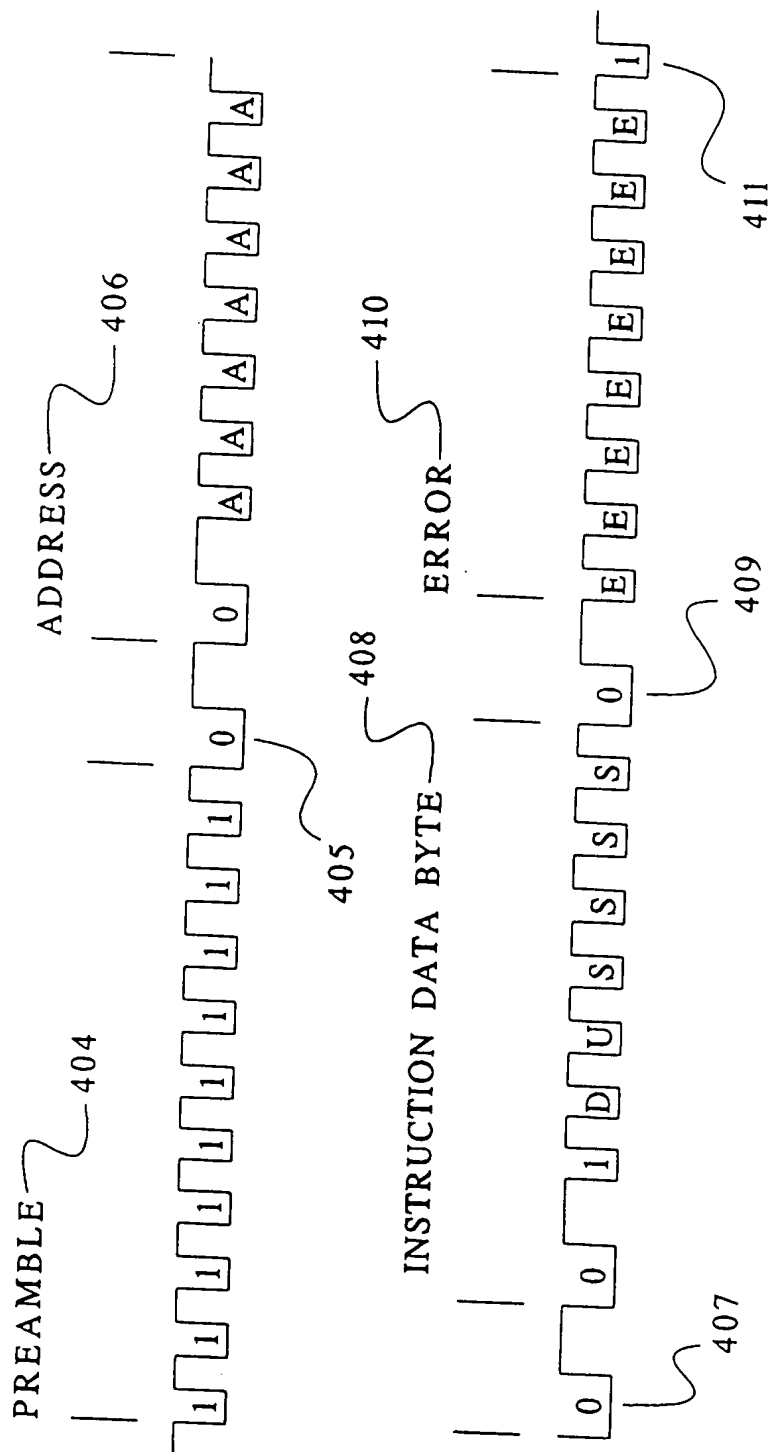


FIG.11

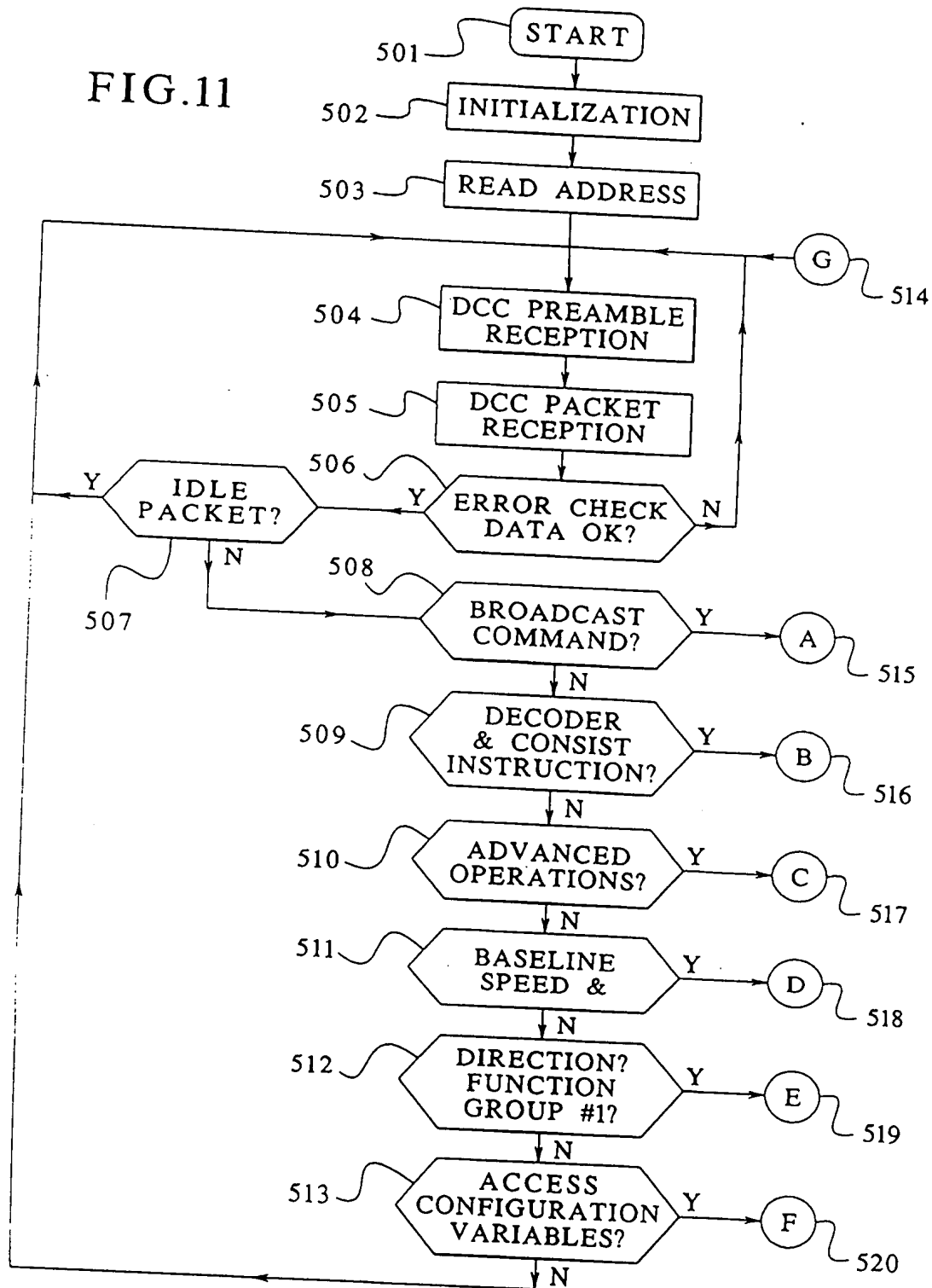


FIG.12

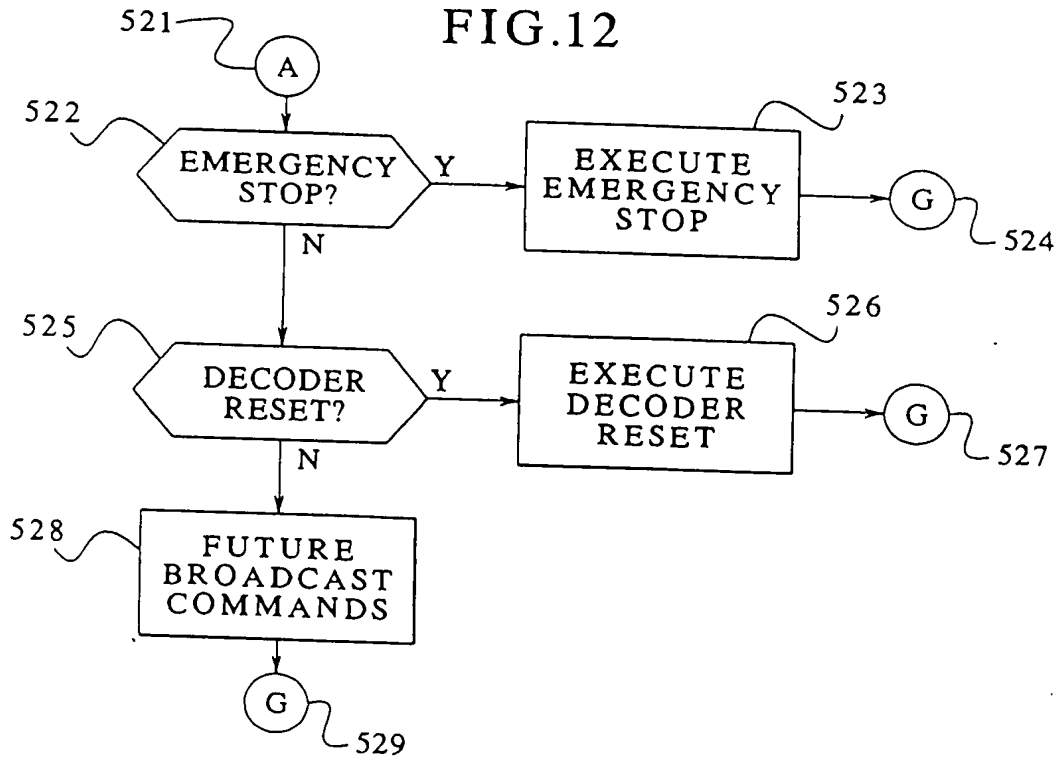
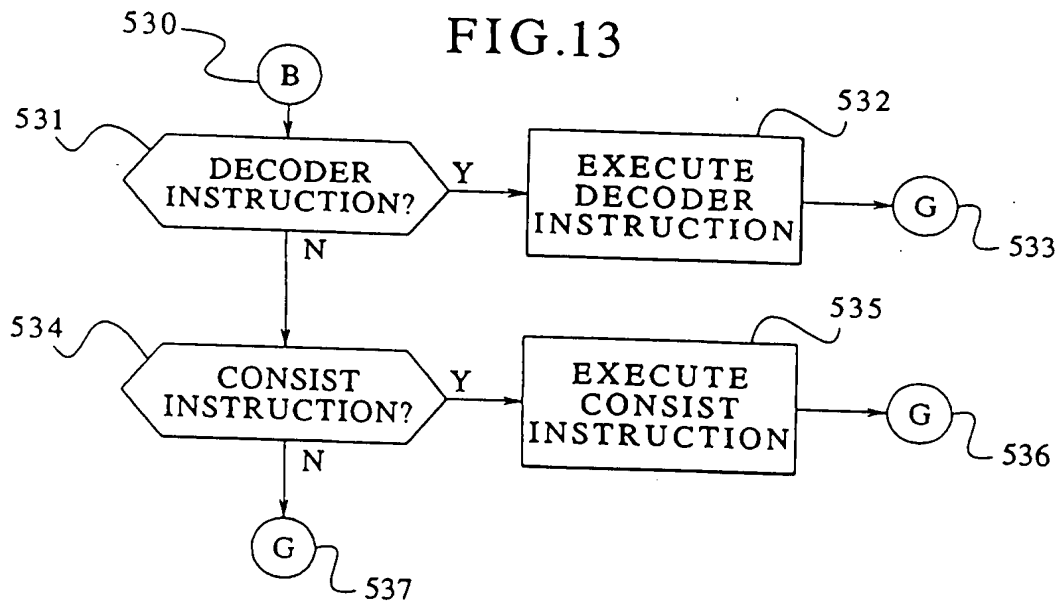


FIG.13



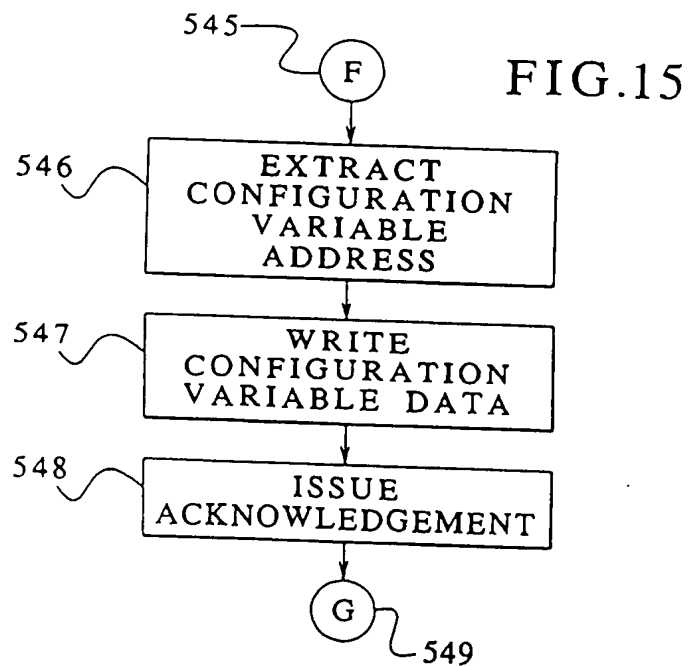
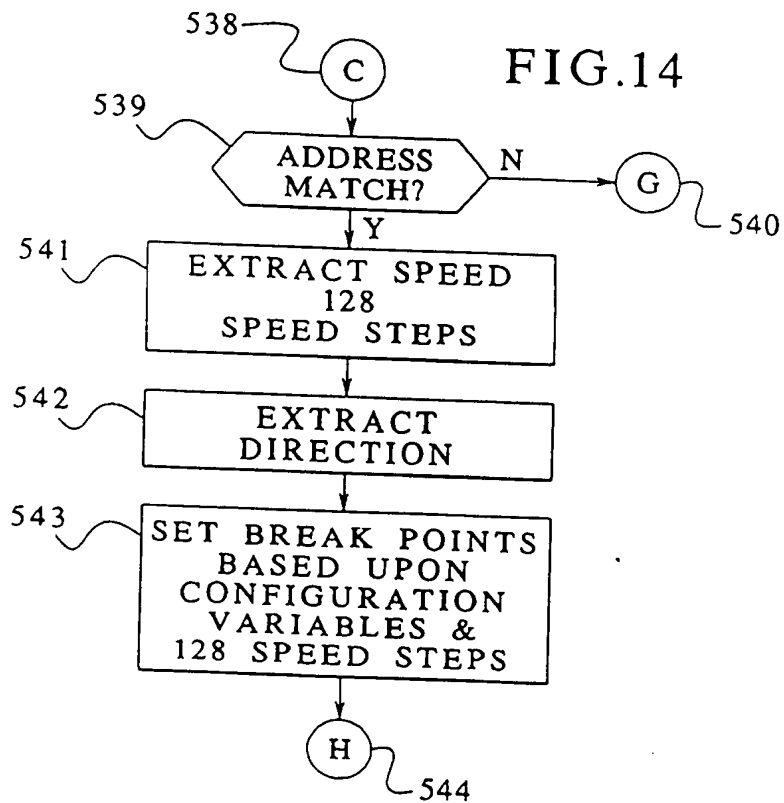


FIG.16

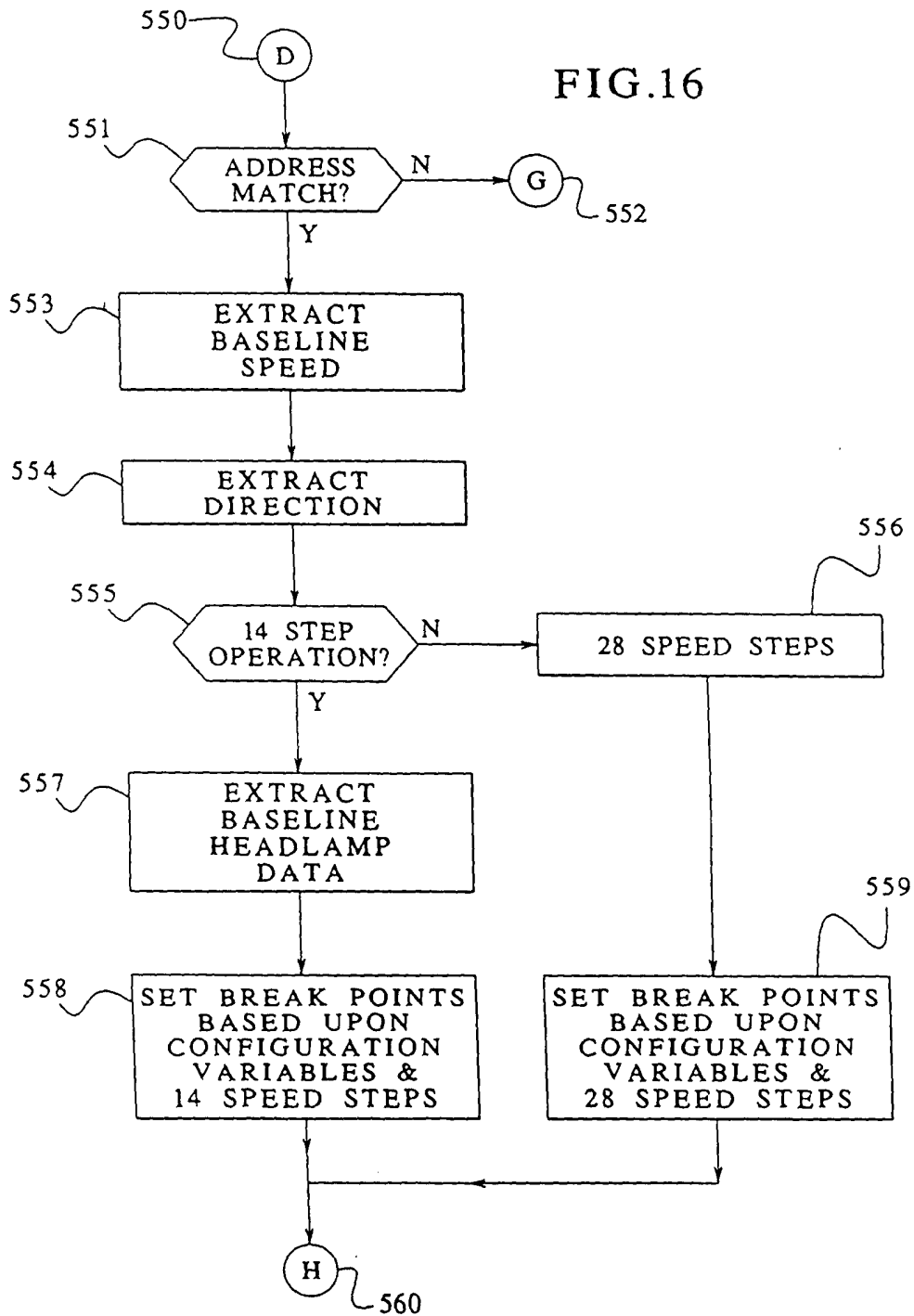




FIG.17

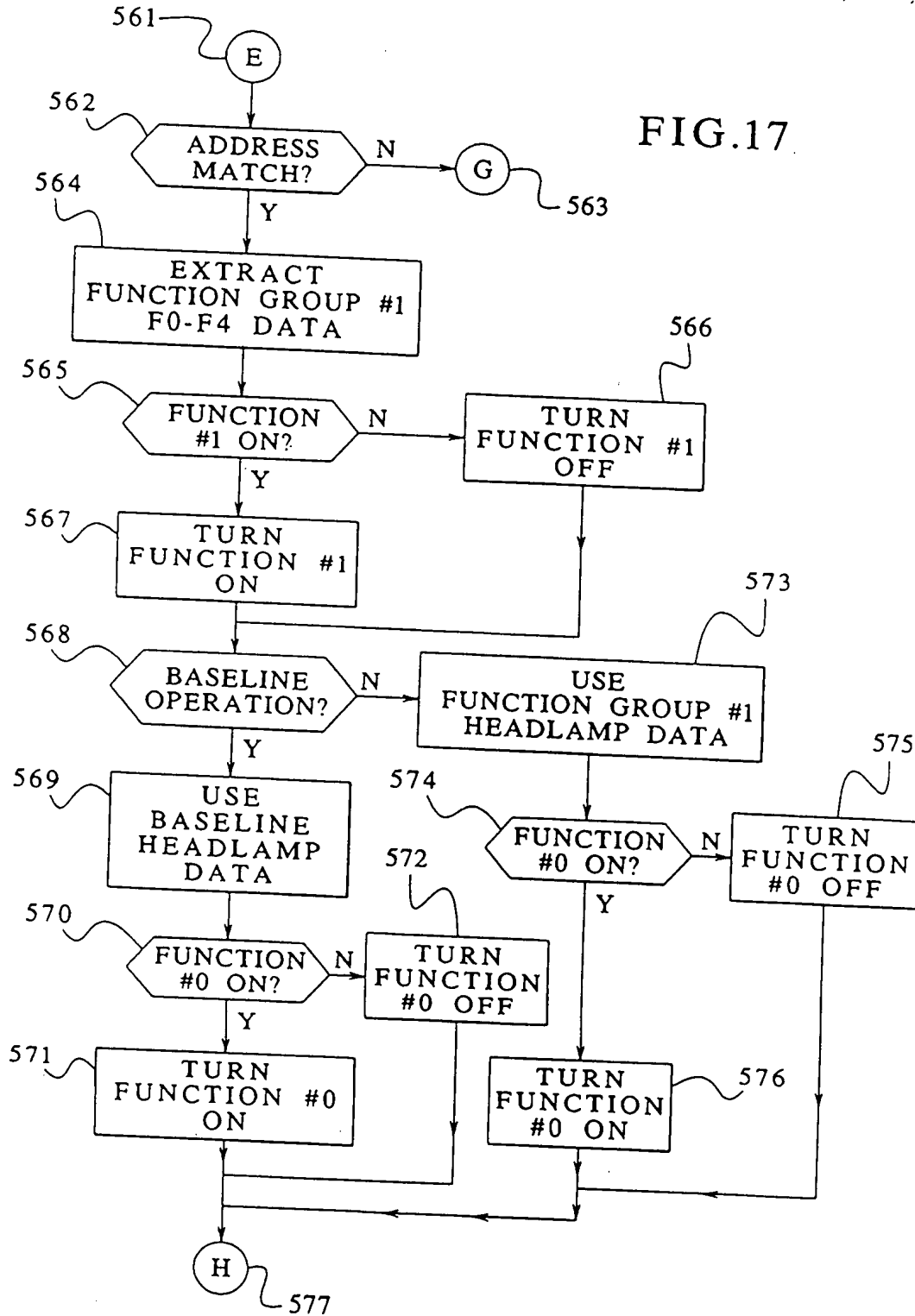
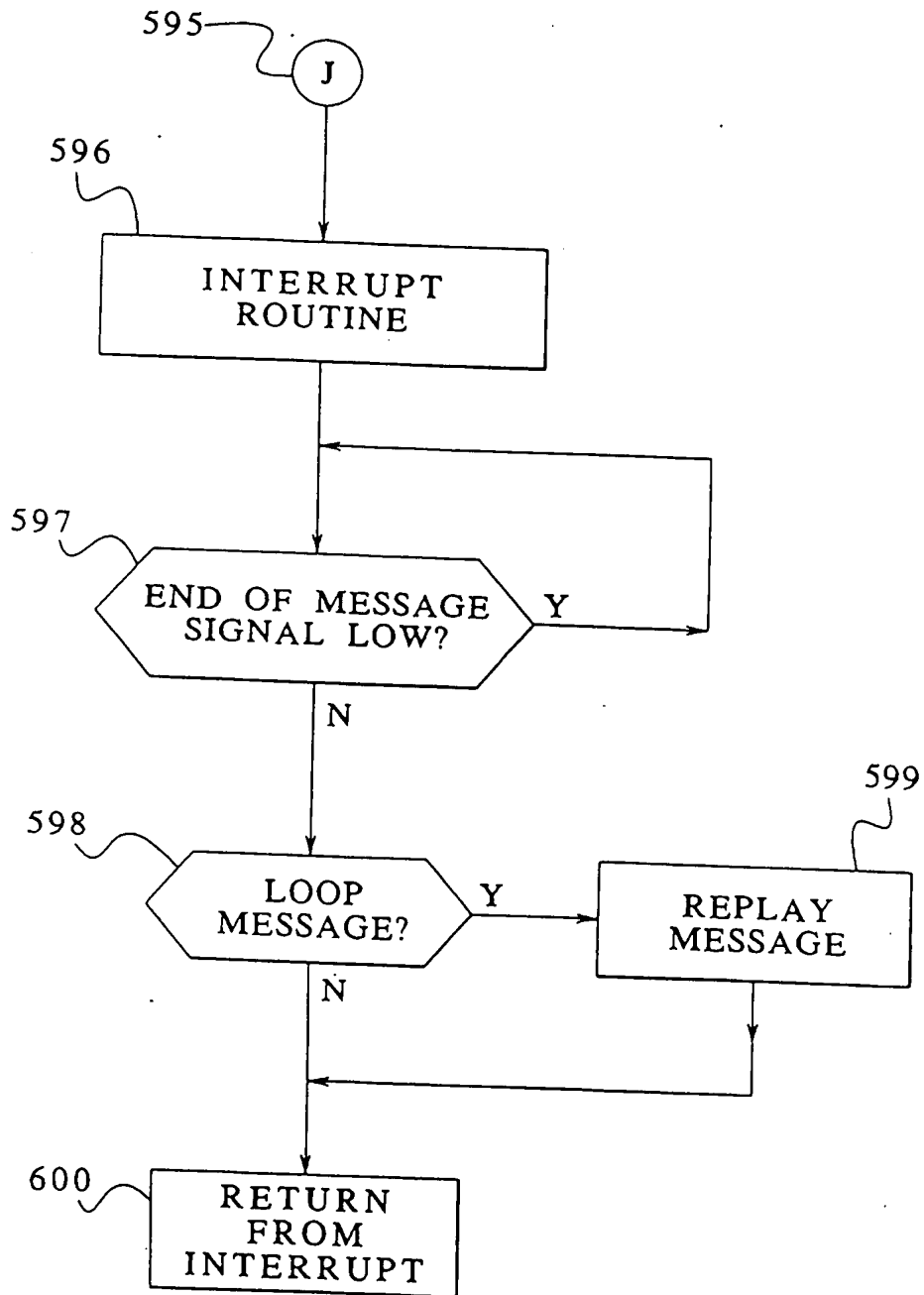


FIG.18



000001-1542550

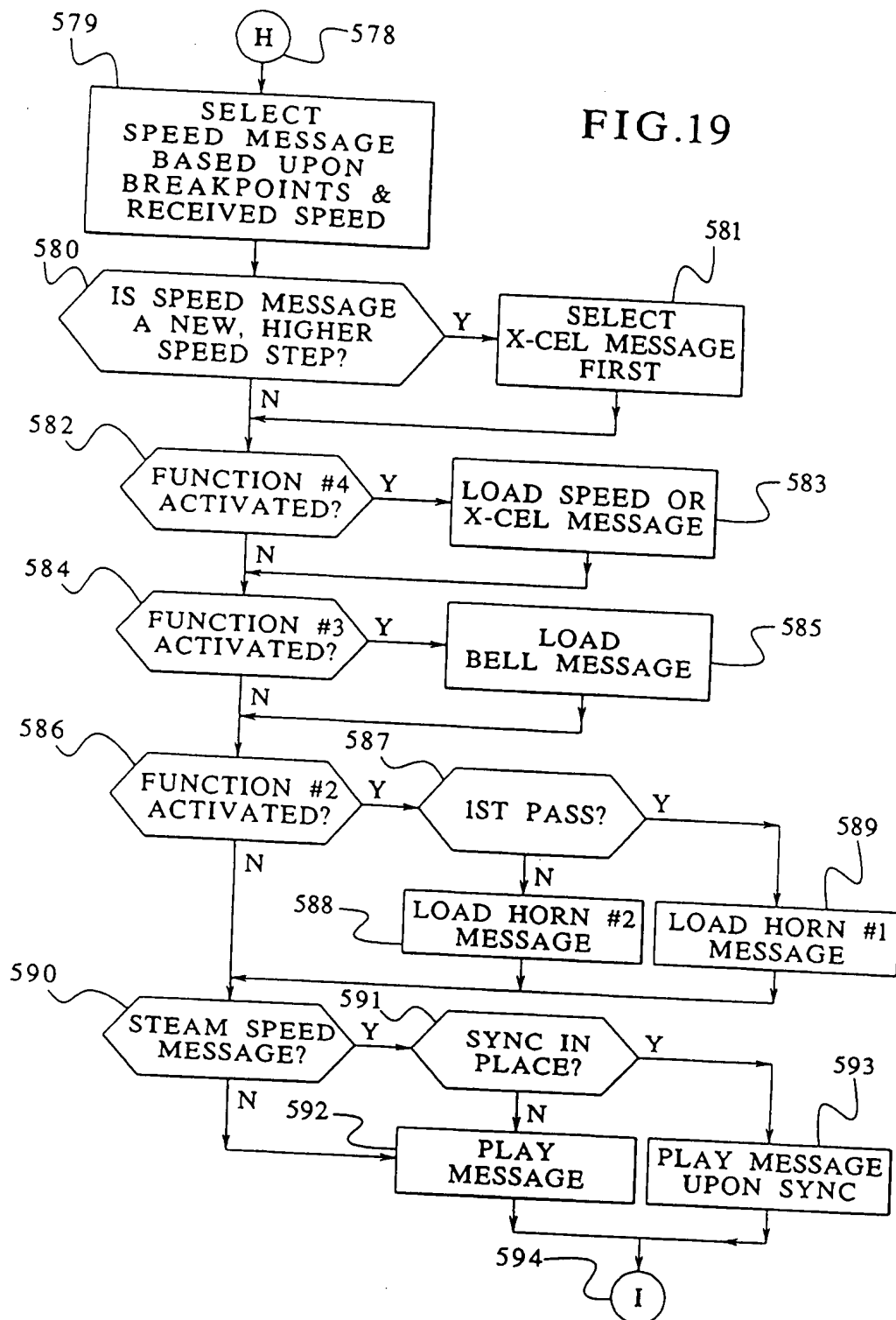


FIG.20

